

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,924,863 B2  
APPLICATION NO. : 10/081800  
DATED : August 2, 2005  
INVENTOR(S) : Nishida et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 2, Column 62, Line 5, " $30 [\mu\text{m}] \leq L(N+1) [\mu\text{m}] \leq 40[\mu\text{m}]$ " should be -- $30 [\mu\text{m}] \leq L/(N+1) [\mu\text{m}] \leq 40 [\mu\text{m}]$ --.

Claim 7, Col. 66, line 48, "pixel electrode" should be --a pixel electrode--.

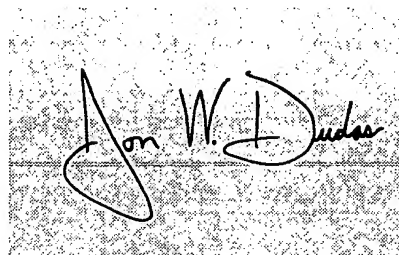
Claim 10, Col. 70, line 43, "field," should be --fields--.

Claim 10, Col. 70, line 52, "substrate" should be --first substrate--.

Claim 10, Col. 71, line 14, "alone" should be --along--.

Signed and Sealed this

Twenty-ninth Day of August, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a rectangular area with a light gray grid pattern.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*